SHEET 1 OF 1

INFORMATION DISCLOSURE CITATION IN AN APPLICATION						ATTY. DOCKET NO. 066365-0020		SERIAL NO. 10/813,615			
						APPLICANT Simon KNOWLES					
(PTO-1449)					FILING DATE March 31, 2004	March 31, 2004 2183					
U.S. PATENT DOCUMENTS											
EXAMINER'S INITIALS		CITE NO.	Document Number Number-Kind Code2 (# known)		Publication Date MM-DD-YYYY	Name of Patentee or Appli Document	Name of Patentee or Applicant of Cited Document		Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
RI	7		US	6,725,357 B1	04-20-2004	COUSIN					
		•	US	6,880,150 B1	04-12-2005	TAKAYAMA et	TAKAYAMA et al.				
			US	5,956,518	09-21-1999	DEHON et a	DEHON et al.				
		• • •	·US	2003/0154358 A1	08-14-2003	. SEONG et a	SEONG et al.				
			US	5,600,810	02-04-1997	OHKAMI	OHKAMI				
			US	5,737,631	04-07-1998	TRIMBERGE	TRIMBERGER				
			US	2002/0010852 A1	01-24-2002	ARNOLD et a	ARNOLD et al.				
			UŞ	2002/0174266 A1	11-21-2002		PALEM et al.				
			ŲS	6,061,367	05-09-2000	SIEMERS				- "	
V			US	2002/0063577 A1	05-30-2002	ABBOTT					
US .											
FOREIGN PATENT DOCUMENTS EXAMINER'S Foreign Patent Document Publication Date Name of Patentee or Pages, Columns, Lines Translation										anslation	
INITIALS		CITE NO.	Country Codes-Number 4-Kind Codes (if known)		MM-DD-YYYY	Applicant of Cited Document			nt .		
			 				 				
			-	· · · · · · · · · · · · · · · · · · ·							
						 	-				

						or, Title, Date, Pertinent Pages, E					
EXAMINER'S INITIALS		CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.								
RF			SIMONEN, et al., "Variable-Length Instruction Compression for Area Minimization," IEEE, 2003.								
				TANENBAUM, "Structured Computer Organization," 1984, pp. 10-12, Prentice-Hall, Inc., Englewood Cliffs, NJ.							
	BEEBE, et al., "Instruction sequencing control," IBM Technical Disclosure Bulletin, May 1972, pp. 3599-3611, Vol. 14, No. 12.										
V	/		ALIPPI, et al., "Determining the Optimum Extended Instruction-Set Architecture for Application Specific Reconfigurable VLIW CPUs," IEEE, 2001, pp. 50-56.								
			 			· · · · · · · · · · · · · · · · · · ·					
/Ryan			EXAMINER Fiegle/			11/29/2006 DATE CONSIDERED					

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.